

Fig.10 Memory control logic

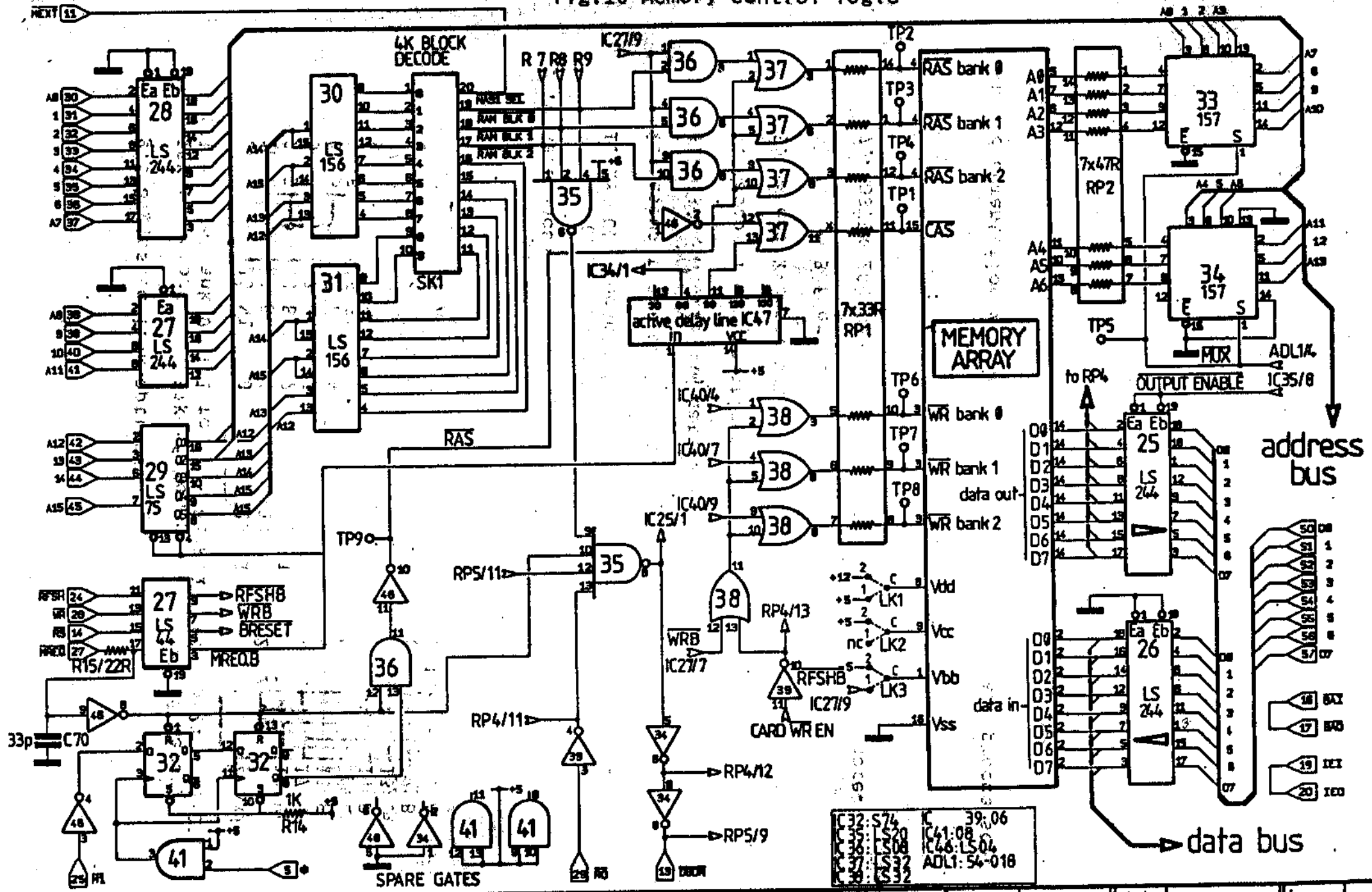
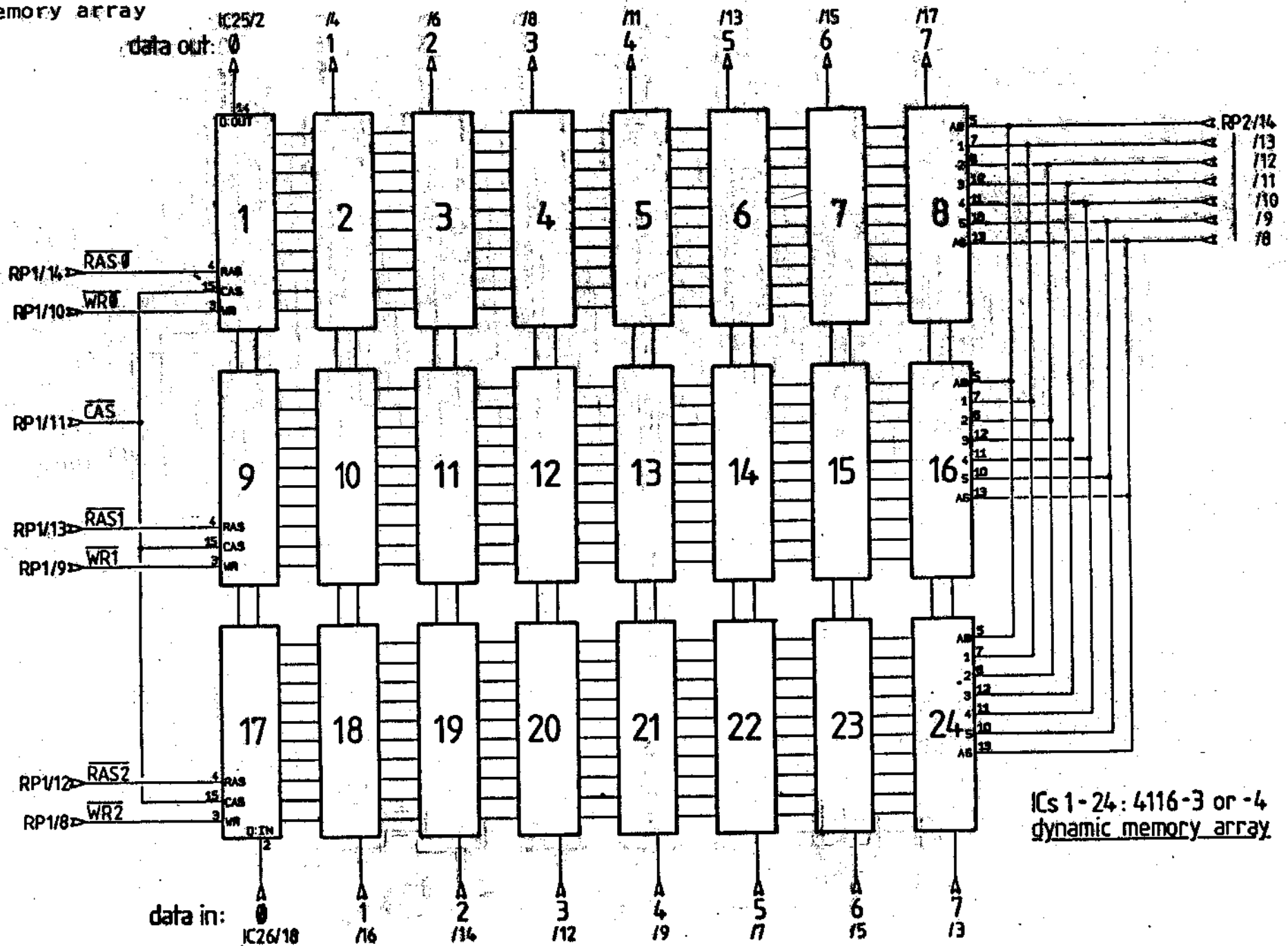




Fig. 12 Memory array



ICs 1-24: 4116-3 or -4  
dynamic memory array